

VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 90

TIMETABLE FOR Ph.D. COURSE WORK, II SEMESTER (R22), REGULAR EXAMINATION – SEPTEMBER, 2025

Branch	1 st September 2025 2:00 PM to 5:00 PM	3 rd September 2025 2:00 PM to 5:00 PM	6 th September 2025 2:00 PM to 5:00 PM	9 th September 2025 2:00 PM to 5:00 PM	11 th September 2025 2:00 PM to 5:00 PM
	Monday	Wednesday	Saturday	Tuesday	Thursday
EEE	Advanced Power Electronic Converters (22PC1PL04)	Digital Controllers in Power Electronic Applications (22PC1PL05)	Advanced Control Systems (22PC1PL06)	Power Quality (22PC1PS05)	Digital Protection of Power System (22PC1PS04)
ECE		Machine Learning (22PC1CP03)	IoT Architecture and System Design (22PC1ES04)	Image and Video Processing (22PE1ES08)	Low Power VLSI Design (22PE1VS11)



Controller of Examinations

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Note : 1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY.
2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.

CONTROLLER OF EXAMINATIONS
VNR VIGNANA JYOTHI INSTITUTE OF
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